

Monolithic Switched-Capacitor Power Converters: Present Trends and Future Predictions

N. Butzen, A. Sarafianos and M. Steyaert







Introduction

Switched-Cap: Trends

Switched-Cap: Present Focus

Conclusions





INTRODUCTION

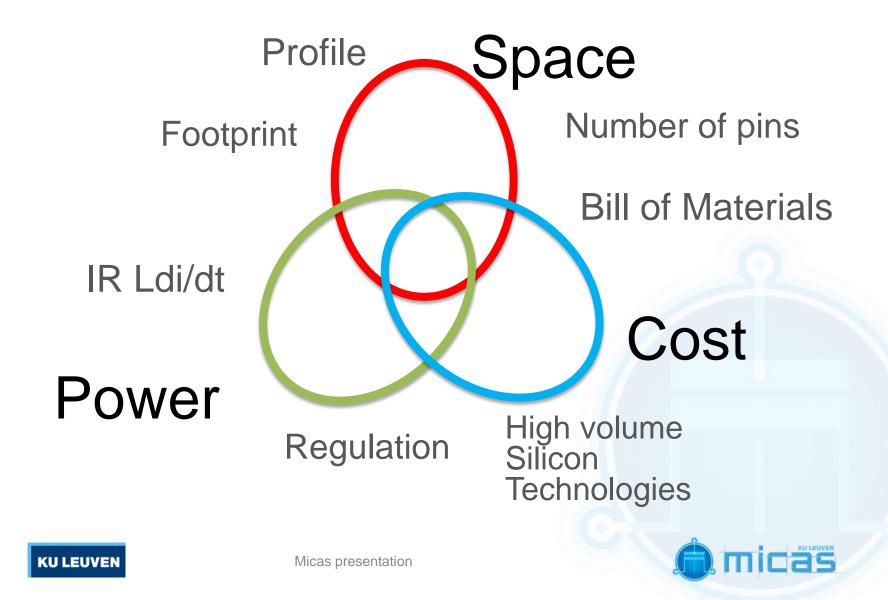




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Why Monolithic?

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Why Switched-Capacitor?

Both Switches and Capacitors are used in Digital Circuits



Readily available



Scale well to advanced nodes





Switched-Capacitor Survey

Select publication titles (TPEL, JSSC, ISSC, APEC, ...)

- A total of 63 publications
- From 1976-2016

M. Steyaert, N. Butzen, H. Meyvaert, A. Sarafianos, P. Callemeyn, T. Van Breussegem and M. Wens, "DCDC performance Survey", [Online]. Available:

http://homes.esat.kuleuven.be/~steyaert/DCDC_Survey/DCDC_PS.html

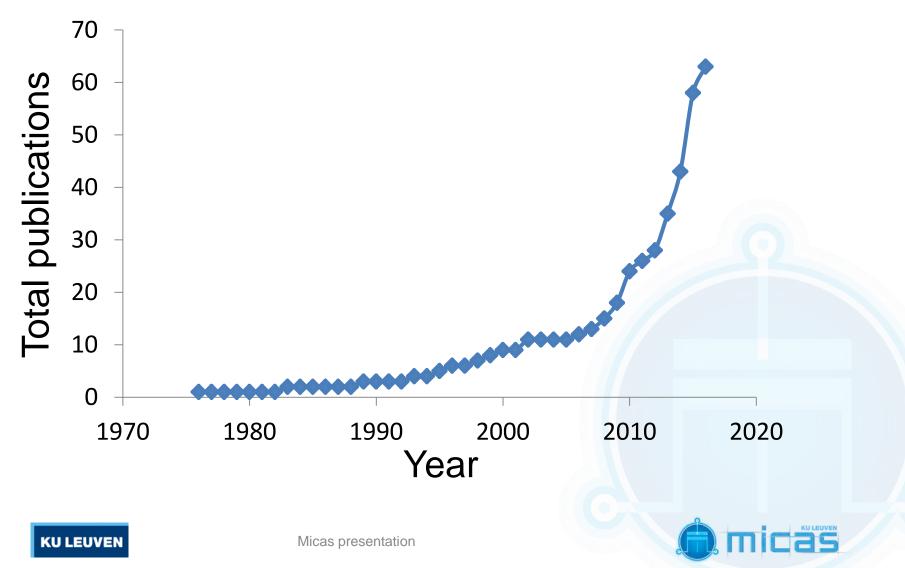


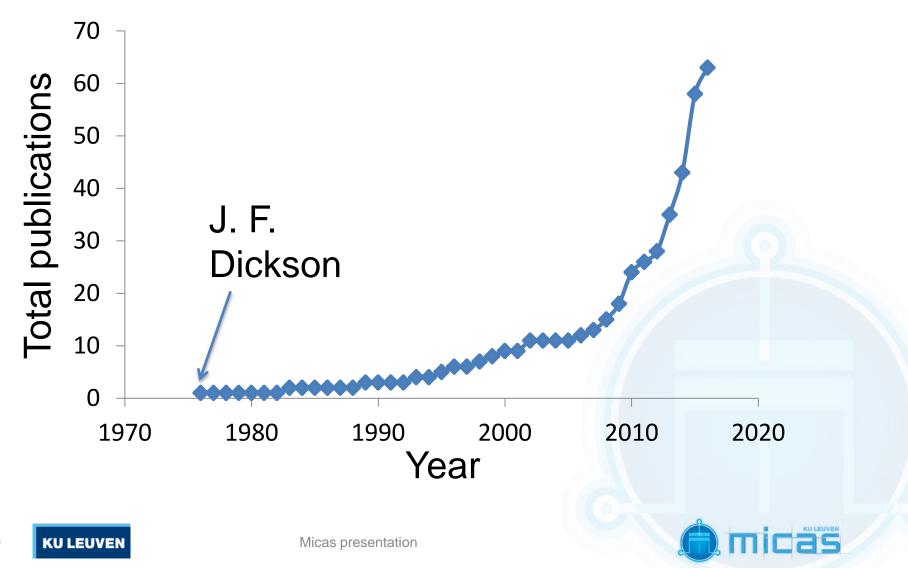
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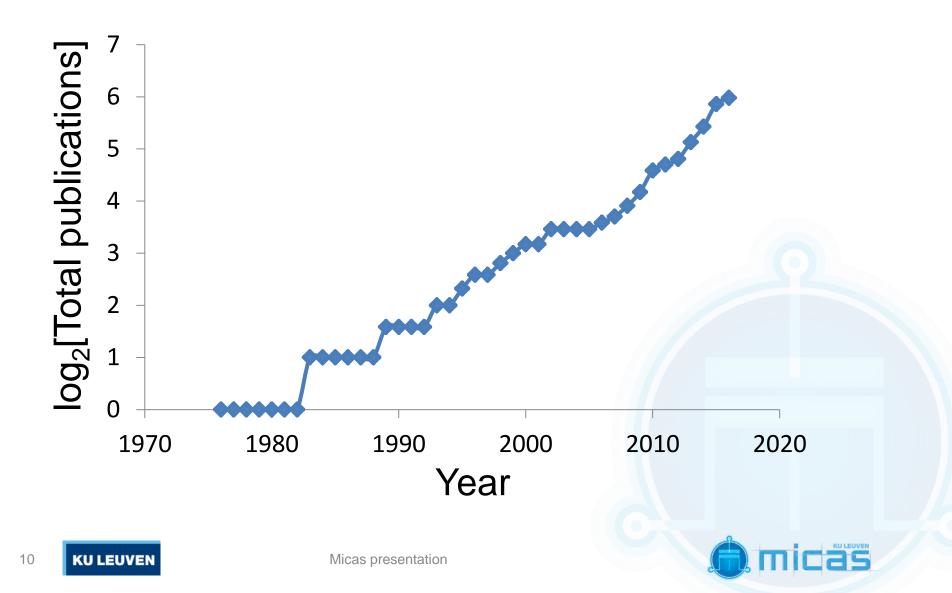
SWITCHED-CAP: TRENDS

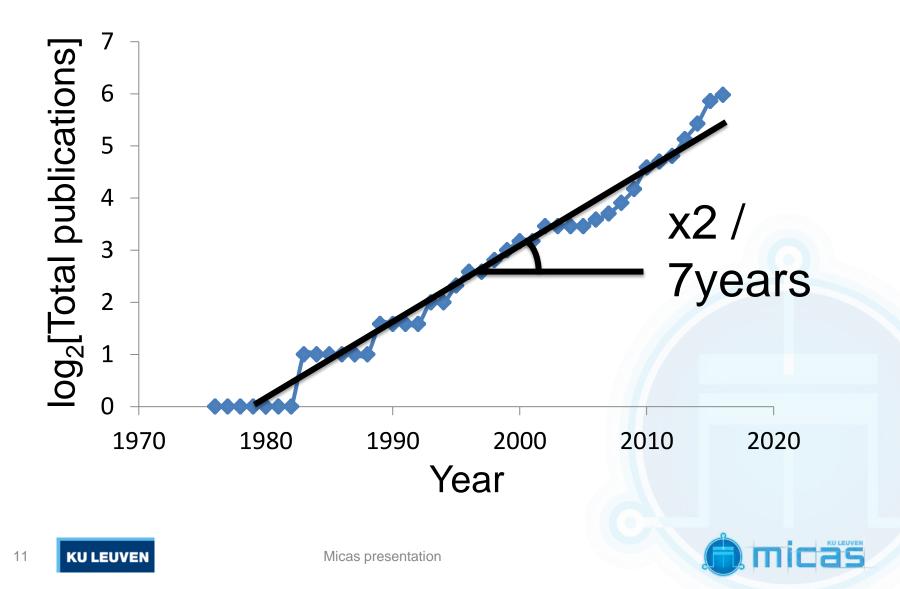


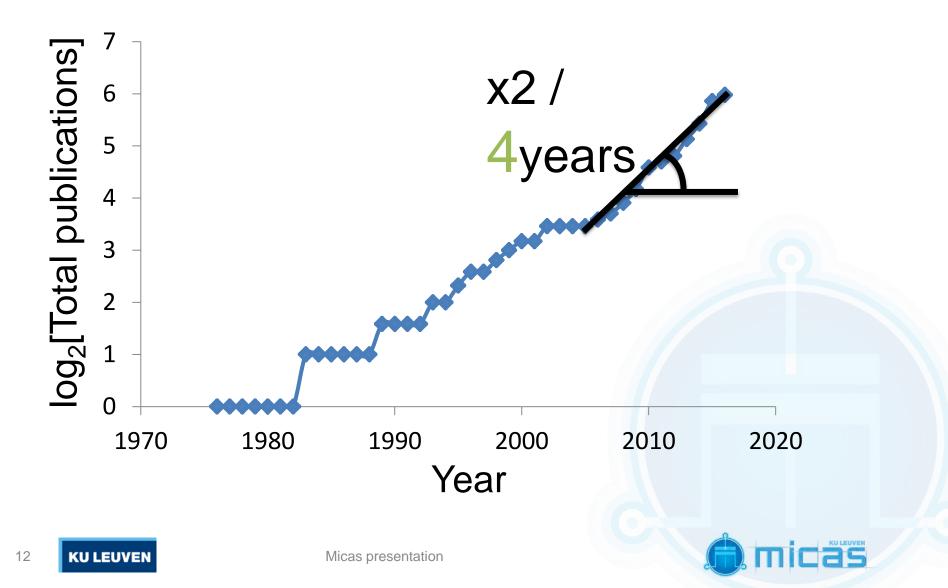


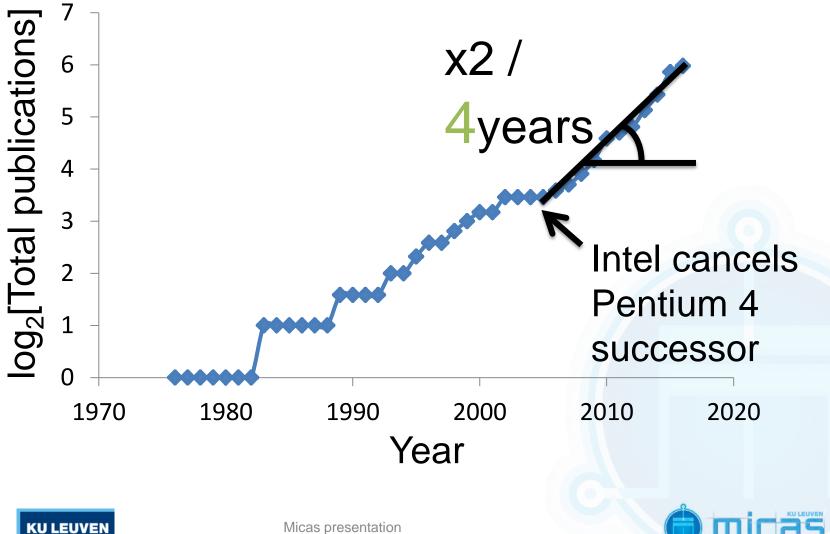




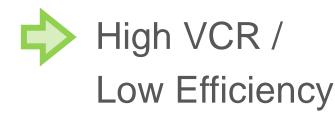








1976-1995 – 'Charge-pumps' – Memory (EEPROM)



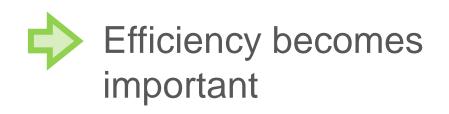
Memory Battery connected device Scavengers Processor/SoC's

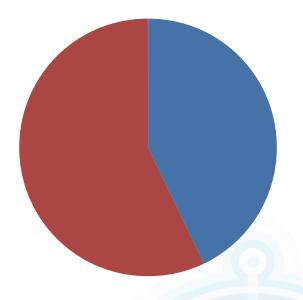
- General
- Other





1996-2006– Portable devices





Memory

- Battery connected device
- Scavengers
- Processor/SoC's
- General
- Other



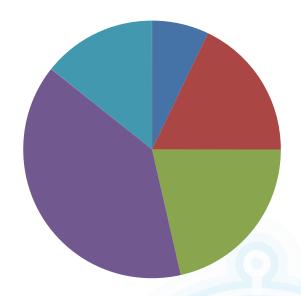


2007-2011

- Systems on Chip
- Energy Harvesting
- General papers



'Allround designs'



- Memory
- Battery connected device
- Scavengers
- Processor/SoC's
- General
- Other





2012-2016

- Processors (DVS/DVFS)
- Automotive/MEMS/ Actuators



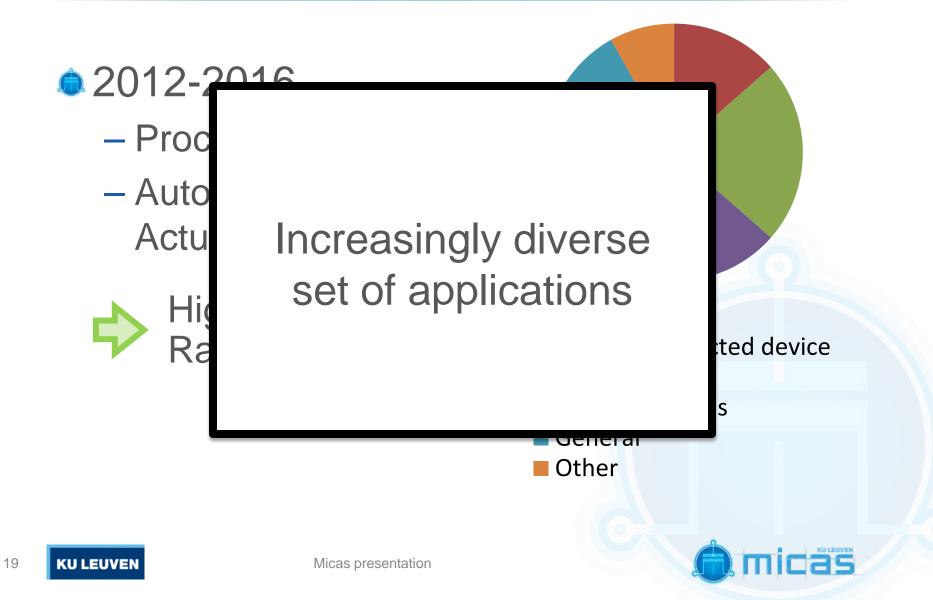
High Power, Voltage Range

Memory

- Battery connected device
- Scavengers
- Processor/SoC's
- General
- Other

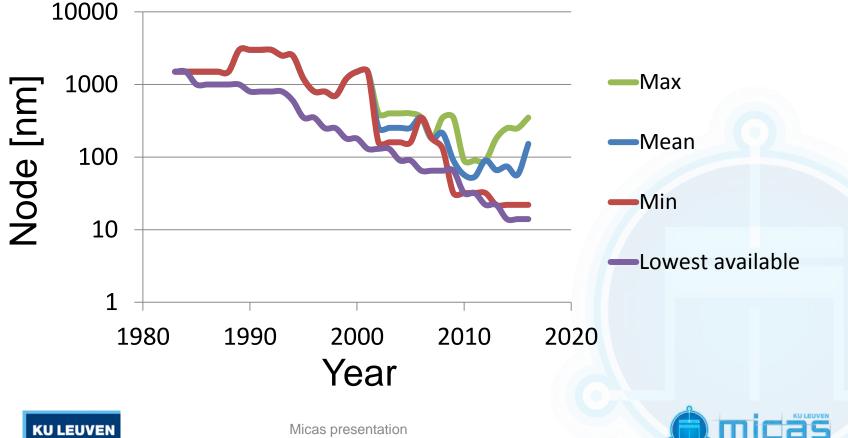






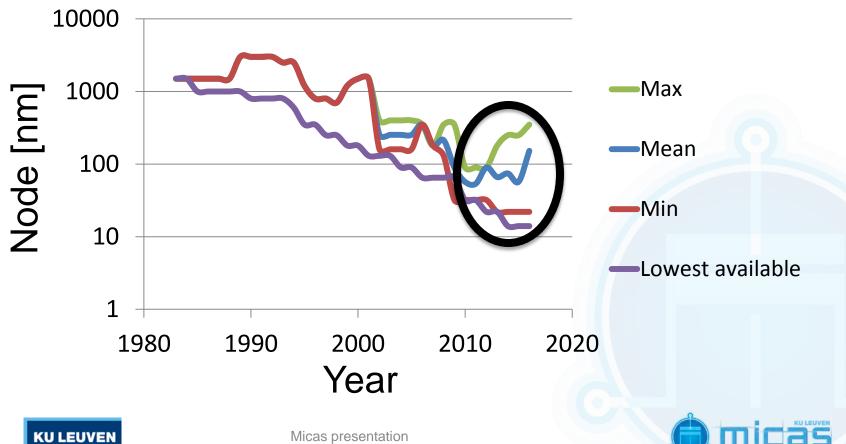
What about technology?

SC's are on the bleeding edge



What about technology?





SWITCHED-CAP: PRESENT FOCUS







Wide efficient Voltage Range

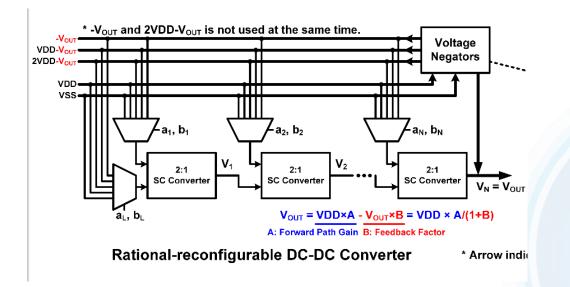
Efficiency/Power Density Limits





Wide efficient Voltage Range

● Efficiency limited by Conversion Ratio → Use a lot of VCR's

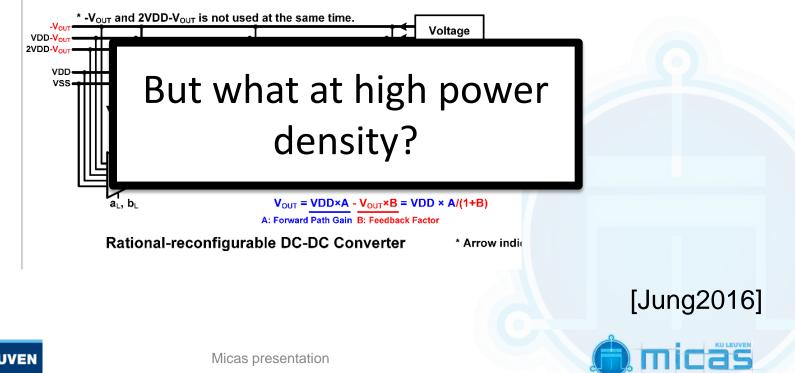


[Jung2016]

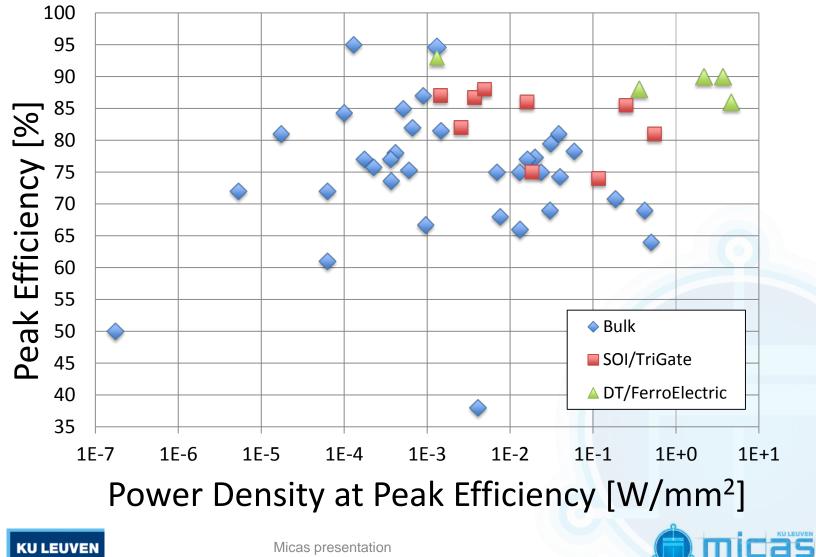


Wide efficient Voltage Range

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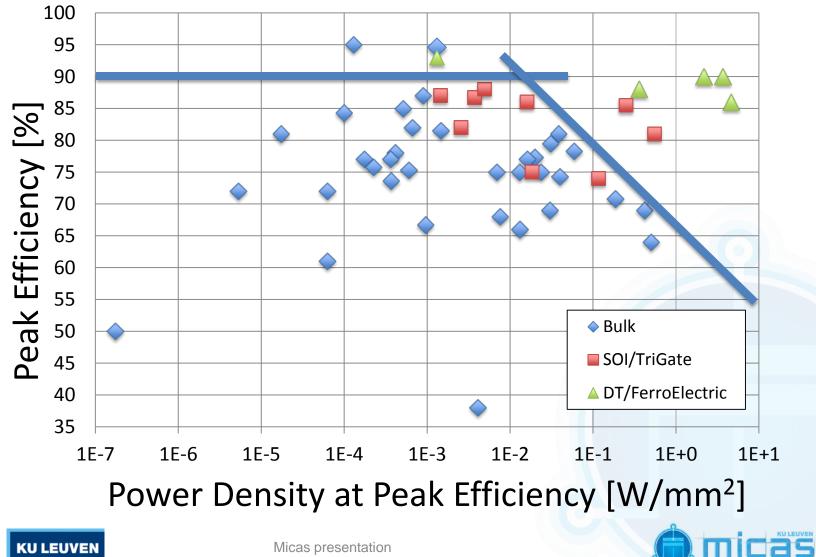


Efficiency/Power Density Limits

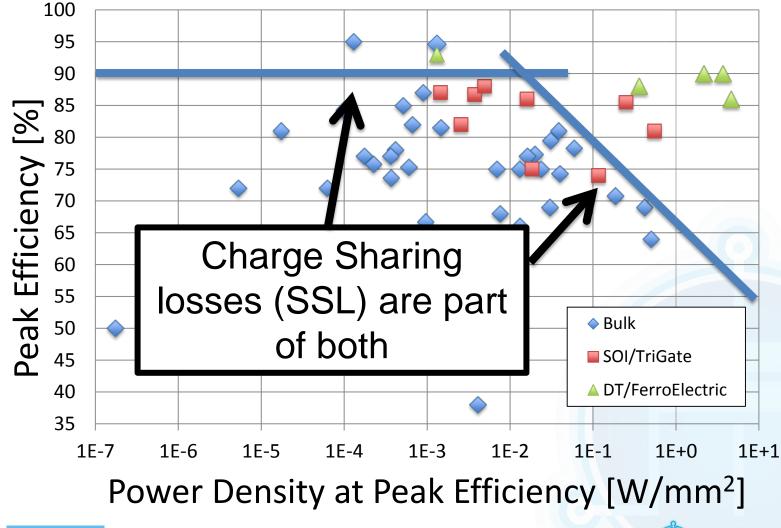




Efficiency/Power Density Limits



Efficiency/Power Density Limits





KU LEUVEN

Pushing The Limits

Three Strategies:

- Hybridization
- Rethink power delivery
- Advanced Multiphasing



Hybridization

Adding Inductor makes Caps Better

- Resonant Converters
- Soft-switching

[Kesarwani2015]

[Lei2013]

Better Efficiency / Output Power Requires External component (potentially PowerSiP)





Rethink power delivery

Change how load gets power (or in which form)

- Voltage Domain Stacking
- Flying Domain CMOS
- Allow large Ripple

- [Rajanpandian2005] [Salem2016]
 - [Zimmer2015]

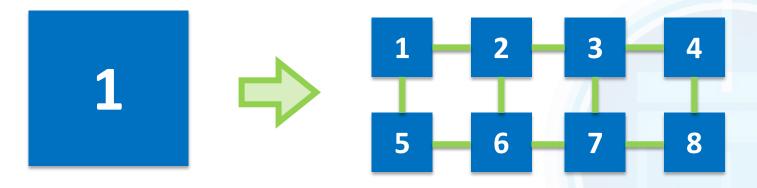
Better Efficiency / Output Power
Requires changes in load design



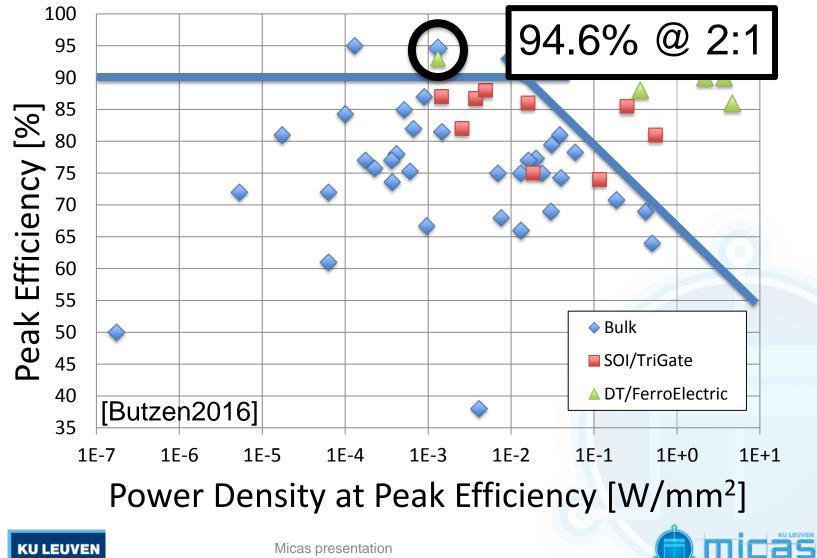
Make use of CMOS' strengths

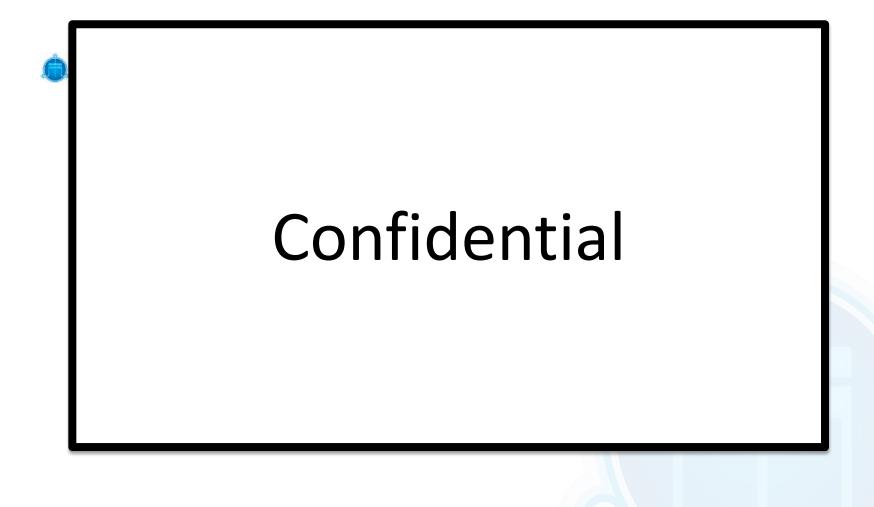
- Low Complexity Cost
- High Frequency ceiling
- Low Fragmentation cost

"Whole is bigger than sum of its parts"



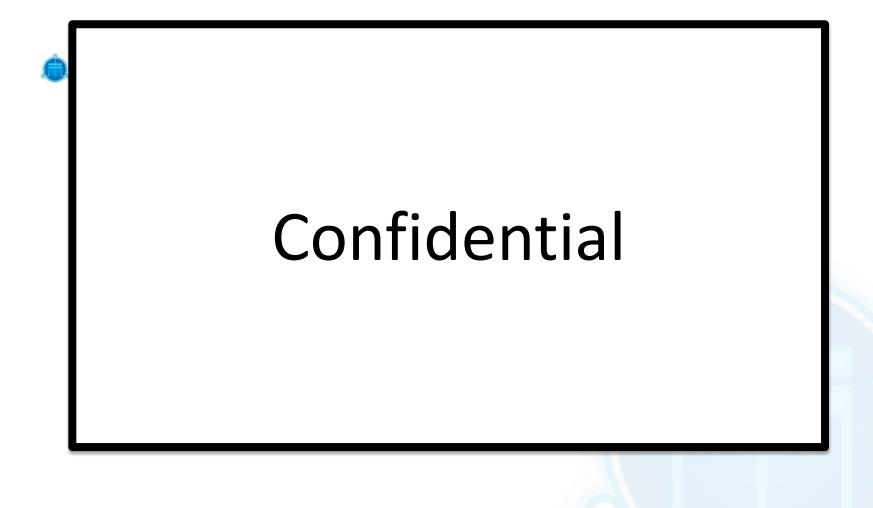
















CONCLUSIONS





Switched Capacitors are...

- (still) on the rise, being continuously used in new Applications
- part of very diverse research area

Many Promising research directions Still a lot of research to be done!



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Thank you for your attention!

QUESTIONS?



