Printed Components (from Nanoparticles) and Large Area Capacitors

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ADEPT Metacapacitors

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and City University of New York Energy Institute (CUNY Energy Institute), New York State Energy Research and Development Authority (NYSERDA), Con Edison (Con Ed)

Team Overview: CUNY Energy Institute



Sanjoy Banerjee, director, Chemical E

Stephen O'Brien, Associate Prof., Materials Science/ Chemistry





Daniel Steingart, assistant professor ,Chem E/Mat Sci



Alex Couzis, Prof., Chem E

Limin Huang, Sr Research Scientist, Materials



Team Overview: Columbia and UC Berkeley



Ioannis Kymissis is an assistant professor of Electrical Engineering at Columbia University, - Columbia Laboratory for Unconventional Electronics, and a member of the DOE EFRC at Columbia University.



Peter Kinget is an associate professor of Electrical Engineering at Columbia University, - integrated circuit and system integration expertise and keen understanding of analog and RF integrated circuits and signal processing using advanced silicon semiconductor processes.



Seth Sanders is a professor of Electrical Engineering and the University of California, Berkeley. His research interests are in high frequency power conversion circuits and components, in design and control of electric machine systems, and in nonlinear circuit and system theory as related to the power electronics field. - novel designs of switch capacitors topologies



Chemical Approaches to Nanomaterials

Making Materials Chemistry a Technology

Atomic or molecular units, with well known subatomic structure in isolation, offer the ultimate building blocks for a bottom-up manufacturing synthesis with applications in many industries.

- Can chemistry provide us with a means to produce technologically useful monolayers, thin films and ultimately more complex devices?



Synthesis of nanocrystals – preparation of nanoscale building blocks



Willis, A. L.; Chen, Z.; He, J.; Zhu, Y.; Turro, N. J.; O'Brien, S. *Journal of Nanomaterials*, **2007**, doi:10.1155/2007/14858; 2. Yin, M.; Chen, Z.; Deegan, B.; O'Brien, S. *Journal of Materials Research* **2007**, *vol.22*, 7, 1987-1995; 3. Andelman, T.; Gong, Y.; Neumark, G.; O'Brien, S. *Journal of Nanomaterials*, **2007**, doi:10.1155/2007/73824; 4. White, B.; Yin, M.; Hall, A.; Le, D.; Stolbov, S.; Rahman, T.; Turro, N.; O'Brien, S. *Nano Letters* **2006**, 6, 2095-2098; 5. Chen, Z.; Huang, L.; He, J.; Zhu, Y.; O'Brien, S. *Journal of Materials Research* **2006**, 21, 3187-3195. 6. Yin, M.; Willis, A.; Redl, F.; Turro, N. J.; O'Brien, S. P. *Journal of Materials Research*, **2004**, 19(4), 1208-1215.; 7. Redl, F. X., Black; C. T.; Papaefthymiou, G. C.; Sandstrom, R. L.; Yin, M.; Zeng, H.; Murray, C. B. O'Brien, S. P. *J. Am. Chem. Soc.*, **2004**, 126(44); 14583-14599.

Obrien Nanoparticle Film Strategy: Increasing Complexity: towards enhancing properties – collective properties



Formation of Binary Nanoparticle Superlattices (BNSLs)

Evaporation Driven Assembly.



Structural diversity in binary nanoparticle superlattices



Made from Pairs of Nanoparticles of Ag, Pd, Au, PbSe, Fe₂O₃.

e.g. (a) 13.4 nm Fe₂O₃ and 5nm Au

Shevchenko, E. V.; Talapin, D. V.; Kotov, N. A.; O'Brien, S.; Murray, C. B. Nature 2006, 439, 55-59.

Binary nanoparticle superlattices

MgZn₂

0.8

0.8



"Structure Direction of II-IV Semiconductor Quantum Dot Superlattices by Tuning Radius ratio", ACS Nano, **2008**, 2(6), 1219-1229.

Superparamagnetic Nanoparticles











1. Magnetic nanostructures: Overcoming thermal fluctuations, Johannes Eisenmenger and Ivan K. Schuller, Nature Materials 2, 437 - 438 (2003); 2. Wiki (superparamagnetism); 3. Monodisperse magnetic nanoparticles for biodetection, imaging, and drug delivery: a versatile and evolving technology, SHIVANG R. DAVE, XIAOHU GAO. ; 4. New J. Phys. 11 (2009) 033034 doi:10.1088/1367-2630/11/3/033034Structural and magnetic deconvolution of FePt/FeO_vnanoparticles using x-ray magnetic circular dichroism D Nolle¹, E Goering^{1,4}, T Tietze¹, G Schütz¹, A Figuerola^{2,3} and L Manna

Scaling Barium Titanate (BaTiO₃) and the Ferroelectric Phase Transition





O'Brien, S.; Brus, L.; Murray, C. B. Journal of the American Chemical Society **2001**, 123, 12085-12086.





FRAM: Non -Volatile Memory





Huang, L.; Chen, Z.; Wilson, J. D.; Banerjee, S.; Robinson, R. D.; Herman, I. P.; Laibowitz, R.; O'Brien, S. *Journal of Applied Physics* **2006**, 100, 034316.

Complex Oxide Nanocrystal Synthesis with Applications in Flexible/Printable Thin Film Technologies

BaTi alkoxide		Alcohol				
•Ba(BaO) + Ti(iPr) ₄ •BaTi ethylhexano- isoproxide •Ba(iPr) ₂ + Ti(iPr) ₄	+	 •ethanol, ethanol with controlled amount of water(5-20wt%) •isopropanol •ethanol +isopropanol 				
		solvothermal: 200-220°C, 1-4 ds				
Individual BaTiO ₃ nanocrystals \rightarrow stable suspension						
hydrophilic surface: cthanol, DMF as solvents hydrophobic surface (oleic acid coating): hexane, toluene as solvents						
adding polymer (optional)		low T processing (spin-coating)				
thin film of BaTiO, nanocrystals or polymer/BaTiO, nanocomposites						



solvothermal process: BaTi alkoxide+ alcohol/water



ethanol suspension of BaTiO₃ nanocrystals



toluene suspension of polymer/nanocrystal nanocomposite





capacitor structure



pentacene transistors



low temperature processing: Individual, well crystallized Nanocrystals as building blocks

Free Standing Films



Barium Titanate Nanocrystal Thin Films and Polymer Composite Films



a) SEM images of a BaTiO₃ nanocrystal thin film (8nm), top view. (inset) a photo image of a homogeneous ethanol suspension of BaTiO₃ nanocrystals (~20 mg/ml); (b) cross-sectional view of the film; (c) SEM image of poly(alpha-methylstyrene)/BaTiO₃ nanocomposite thin film, top view. (inset) a photo image of a transparent poly(a-methyl styrene)/BaTiO₃ nanocomposite solution (~ 20 mg/ml, 8 nm, weight ratio of polymer/BaTiO₃ = 1), and a TEM image of the uniform polymer/BaTiO₃ nanocomposite; (d) SEM image of poly(alpha-methylstyrene)/BaTiO₃ nanocomposite thin film, top view. (inset) a cross-section view of the uniform polymer/BaTiO₃ nanocomposite thin film and a photo image of a semitransparent BaTiO₃ nanocrystal solution (~ 20 mg/ml, 25 nm).

BT and BST Thin Film Capacitors



Thin film sample	Crystal size (nm) Dielectric constant (measured at 100kHz)		
BaTiO ₃	8-12	22-25	
BaTiO ₃ /parylene	25-30	34	
Ba _{0.7} Sr _{0.3} TiO ₃	8-12	10	
Ba _{0.7} Sr _{0.3} TiO ₃ /parylene	25-30	13	
	8-12	27	
	25-30	47	
	8-12	12	
	25-30	16	



Ferroelectric measurements of assembled nanocrystal thin films of BT and BST in Sawyer Tower configuration under conditions of (a) demoisturized films with an evaporated film of parylene, dried at 60 °C (b) RT and immersed in liquid N_2 for the BT/parylene thin film.

1. Huang, L.; Zhang, J.; Kymissis, I.; O'Brien, S. "High K thin films built from uniform Barium Titanate Nanocrystals in the Superparaelectric Limit" *Adv. Func. Mater.* **2010**, 20, 554-560.

Concept: Metacapacitors[™]

• Goals:

- In-structure integrated circuits can make programmable switched capacitor arrays
- Processing can be scaled using roll-to-roll coating techniques
- long lifetime, high power, high energy density, flexible energy storage, conversion, and conditioning



Switched Capacitor Design

Magnetic-based power converters are currently the common solution for higher power applications (1W and above)

The use of switched-capacitor (SC) DC/DC conversion circuits (a.k.a. charge pumps) has been limited to very low power levels.

For practical switching frequencies, the power levels of SC designs can only be increased by using larger capacitors.

SC designs have a number of important advantages as long as cheap and dense capacitors are available and they can outperform magnetic-based converters for medium to high conversion ratios.

Project to make switched-capacitor DC/DC converter amenable to address a wide range of new applications in power conversion.



Novel Power Conversion Circuit Topologies

The circuit architectures to be exploited in the Metacapacitor program rely on switched capacitor dc-dc and multi-level inverter conversion topologies. Switched capacitor dc-dc converter topologies have been the poor step-sister technology to the industry dominating magnetic-based power conversion topologies due to many factors. Some of these pertain to sunken investments and mature design strategies built on standardized designs for key components: switch technologies (eg. Si MOSFET and IGBTs); ferrite, powdered-metal, and laminated metal magnetic cores; and CMOS based analog and digital PWM controllers.

However, the all capacitor approach is especially effective due to the higher energy density of many capacitor technologies in comparison to compact magnetic devices. Given the exceptional energy densities of the capacitors being developed in this Metacapacitor program, the advantage of the all capacitor topologies is further accentuated.

Application example: PV inverter topology - single PV panel (eg. ~30 Vdc @ 200 W) with a local stabilizing Metacapacitor (20-100 mF for 120 Hz ripple filtering) and a power MOSFET H-bridge to form a single submodule.

Printed/Spin Coated Thin Film Capacitors

The key requirement in the formation of the BST nanoparticle based capacitors is the consolidation of the nanoparticles into dense interconnected structures upon deposition onto a flexible conductively coated substrate. Requirements emerge from this approach:

- (i) that the particle form dense monolayer clusters;
- (ii) that multilayers can be deposited with appropriate registry
- (iii) that the coating approach provides appropriate lateral position control
- (iv) the process must be scalable, materials agnostic, and economically viable. Gravure and rotogravure printing of nano-colloid inks is a process that satisfies these requirements and provides a direct path to process scalability and lost cost.
- (v) Preparation of Multilayer Capacitors is possible



Collaboration and Acknowledgements

Project Faculty Ioannis Kymissis Dan Steingart Alex Couzis Sanjoy Banerjee (CUNY Energy Institute

Ioannis Kymissis Peter Kinget (CU)

Seth Sanders (UCB)

Faculty Jackie Li (CCNY) Nick Turro (CU)

Graduate Students: Andrew Byro (CCNY) Benedette Adewale (CCNY) Zhang Jia (CU) Mitchell Kline (UCB) Olivia Niitsoo (CCNY) Research Fellows Dr. Limin Huang (Energy Institute and City College) Dr. Jorge Morales (City College)

> Post Docs Brian Tull (Columbia) Lev Sviridov (Energy Institute)











Novel Power Conversion Circuit Topologies

2. LED Driver Topology (ADEPT)

A Metacapacitor solid-state LED driver topology is illustrated in Figure 8. Possible target applications are single-unit screw-in Edison replacement lamp devices rated at about 15 W, as well as higher power commercial lighting fixtures.

A bulk storage capacitor in the range of 25-100 uF, rated for 200 Vdc, is convenient for the dc bus to handle 120 Hz ripple. The controls and switches can be implemented on a single CMOS IC with devices rated for handling voltages indicated, and junction isolation to provide for the overall voltage stand-offs to the substrate.

The switched capacitor approach enables graceful low power operation by simply reducing clock rate, thus enabling a wide range of dimming without sacrificing efficiency.



Novel Power Conversion Circuit Topologies

1. PV Inverter (Not Part of ADEPT Scope)

PV inverter topology is conceptually illustrated merges a single PV panel (eg. ~30 Vdc @ 200 W) with a local stabilizing Metacapacitor (20-100 mF for 120 Hz ripple filtering) and a power MOSFET H-bridge to form a single submodule. The diagram of only four submodules which enable nine-level modulation at the inverter output, a representative rooftop installation rated for 3 kW might be comprised of 15 submodules. An array of 15 such submodules would allow a peak output voltage capability of roughly 450 Vdc, making an interface with a 220 Vrms line quite convenient.

This proposed architecture provides all the best features required for a highly reliable distributed PV line interface. These include peak power tracking on a panel by panel basis (i.e. "micro-inverter" functionality), no use of electrolytic capacitors, no substantial use of magnetic devices, highest possible panel efficiency, very low electromagnetic interference (EMI), fault tolerance and high safety potential.



Gate Dielectric: Thin Film TFTs on Flexible Substrates Using Nanocrystal Films of BT/BST (Collaborator Ioannis (John) Kymissis



Transport characteristics of OFETs on flexible PEN substrate, with 200 nm BST and 53 nm parylene C as the gate dielectric (effective dielectric constant is 11.3). (a) is the drain IV curves under different gate biases. The device has an on/off ratio 10⁴ and mobility 0.25 cm²/(Vs) in the saturation region fitted using the data from (b). Leakage characteristics: (9– 12 nA/cm², measured at 5Vdc for a 1mm² area

High k materials - capacitors Innovation in Core Technologies for Power Conversion and Energy Storage



Metacapacitors

City College of New York/Columbia University Metacapacitors[™], Stephen O'Brien, Dan Steingart, Ioannis Kymissis, Peter Kinget, Alex Couzis

STATUS QUO

INSIGHTS

EV

Z

		PROPOSED GOAL(S):			Lagend	
Currently there exists a fundamental tradeoff between chemical storage and charge separation •Chemical bonds store energy, but		nrough the combinati materials synthesis a device fabrication, we with compelling ene density.	v T to tors wer PL	Legend I Development I Development		
 Kinetically limit pot Charge separation quick power, but cannot store 		Batteries	Ultracapacitors	Metacapacitors		
temporally or spatially a	Programmable	No	No	Yes	100 1000 10000 Power Density (W/kg)	
chemical bonds	Energy Density Range (Wh/kg)	10-500	1-10	1-100	als production will be ed to allow for large	
	Power Density Range (W/kg)	10-500	1000-8000	1000-20000	pacitor production. apacitors will be printed b a roll-to-roll process	
Cycle Life	Cycle Life	500-3000	> 1,000,000	> 1,000,000		
Self Assembled Nat complex oxide Film	Whr/kg over life	50,000 to 900,000	1,000,000 — 10,000,000	1,000,000 – 100,000,000		
dielectric properties by paraelectric effect	Market Saturation?	Mature	Moderate	Nascent	fully functional	

Barium Titanate Nanoparticles exhibit excellent dielectric properties

Through low temperature processing such particles can be assembled into devices with exceptional energy density and power density outperforms conventional capacitors while maintaining excellent power density

ASSUMPTIONS AND LIMITATIONS:

• We assume we can create a system with an effective dielectric constant of 50

• We assume we can manufacturing such capacitors in a large scale roll-to-roll process

Demonstration of long term charge retention at or above EDLC capacity, with the power density of a capacitor

END-OF-PHAS

MetacapacitorsTM: Background

- We have a new process for making high k dielectric films at room temperature based on solution control processing.
- We have demonstrated high value/low hysteresis capacitors with high voltage tolerance.
- The substrate and device design allows full integrated device manufacture.
- The principal of E = CV²: The energy stored scales with the square of the voltage. We propose a "metacapacitor" – the energy density of a battery or ultracapacitor but the discharge and lifetime of a ceramic capacitor



Homogeneous solvent suspensions of BaTiO3 nanocrystals (inks) are deposited as thin films through evaporative driven self assembly. Alternatively facile incorporation into a range of high k/ low k polymers can generate a series of nanocomposites.